



Łukasiewicz
Instytut
Mikroelektroniki
i Fotoniki

Warsaw, 26.08.2024 r.

**Request for contract value estimation of two manufacturing runs of
50 samples each of prototype integrated circuits**



For the purpose of the contract value estimation Łukasiewicz Research Network – Institute of Microelectronics and Photonics requests information on the order described below.

Disclaimer!

This contract value estimation request does not constitute an offer within the meaning of Article 66 of the Polish Civil Code and is neither the advertisement nor the price quotation within the meaning of the Public Procurement Law. This information is requested solely for market recognition and pricing estimation for the delivery to be contracted.

1. The Contracting Authority

**Lukasiewicz Research Network – Institute of Microelectronics and Photonics
al. Lotników 32/46,
02-668 Warszawa**

2. Order description

The subject of the order to be contracted is the supply of two manufacturing runs of 50 units each of prototype integrated circuit with a surface area of 25 square millimeter, manufactured in X-FAB Silicon Foundries SE production facility, using XP018 silicon technological process, according to the design provided by the ordering party, in accordance with the technical specifications of the ordering party.

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The 1st manufacturing run

#	Feature	Value
1	Target silicon foundry	Integrated circuit manufacturing process performed within X-FAB Silicon Foundries SE
2	Target silicon process characteristics: name and process modules	XP018 silicon process including modules: LP5MOS, MET3, METMID, MIM, NVM, ISOMOS, MRPOLY, HRPOLY
3	Wafers backside grinding possibility	Wafers backside grinding down to 300 microns (typical)
4	Number of manufacturing samples within a run	Number of manufacturing samples within a run is 50
5	Start of the manufacturing run	The manufacturing run starts (tape-in) in September or October 2024
6	Legal limitations on samples usage / application	No legal limitations on military / defence end use of the integrated circuits manufactured according to the design provided by Ordering Party and delivered by the Contractor

The 2nd manufacturing run

#	Feature	Value
1	Target silicon foundry	Integrated circuit manufacturing process performed within X-FAB Silicon Foundries SE
2	Target silicon process characteristics: name and process modules	XP018 silicon process including modules: LP5MOS, MET3, METMID, MIM, NVM, ISOMOS, MRPOLY, HRPOLY
3	Wafers backside grinding possibility	Wafers backside grinding down to 300 microns (typical)
4	Number of manufacturing samples within a run	Number of manufacturing samples within a run is 50
5	Start of the manufacturing run	The manufacturing run starts (tape-in) in January or February 2025

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6	Legal limitations on samples usage / application	No legal limitations on military / defence end use of the integrated circuits manufactured according to the design provided by Ordering Party and delivered by the Contractor
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3. Contract value estimation components:
The Contractor should include in the quotation:
 - 1) Name, Contractor street address, contact person;
 - 2) The price in PLN /EUR /USD /GBP (net + VAT), including all costs of the contract.

4. Form of submission of the contract value estimation:
Electronically to the following address:

grzegorz.janczyk@imif.lukasiewicz.gov.pl

5. Deadline for contract value estimation: **30.08.2024 r.**

6. Contact person:
grzegorz.janczyk@imif.lukasiewicz.gov.pl

